







Membership Publications/Services Standards Conferences Careers/Jobs Welcome **United States Patent and Trademark Office**  $\bigcirc$ FAQ Terms IEEE Peer Review Quick Links Welcome to IEEE Xplore® C Home Your search matched 10 of 1037503 documents. — What Can A maximum of 500 results are displayed, 15 to a page, sorted by Relevance I Access? Descending order. C Log-out **Refine This Search: Tables of Contents** You may refine your search by editing the current search expression or enteri new one in the text box. — Journals & Magazines bdd based symbolic Search )- Conference ☐ Check to search within this result set **Proceedings** O- Standards Results Key: JNL = Journal or Magazine CNF = Conference STD = Standard Search O- By Author O- Basic 1 Decomposed symbolic forward traversals of large finite state machi Quer, S.; Cabodi, G.; Camurati, P.; O- Advanced Design Automation Conference, 1996, with EURO-VHDL '96 and Exhibition, Proceedings EURO-DAC '96, European , 16-20 Sept. 1996 Member Services Pages:170 - 175 ( ) Join IEEE C - Establish IEEE [Abstract] [PDF Full-Text (564 KB)] IEEE CNF Web Account 2 Dynamic scheduling and clustering in symbolic image computation O- Access the Cabodi, G.; Camurati, P.; Quer, S.; **IEEE Member** Design, Automation and Test in Europe Conference and Exhibition, 2002. Digital Library Proceedings , 4-8 March 2002 Pages: 150 - 156 Print Format [Abstract] [PDF Full-Text (333 KB)] **IEEE CNF** 3 Reliability evaluation of combinational logic circuits by symbolic simulation Bogliolo, A.; Damiani, M.; Olivo, P.; Ricco, B.; VLSI Test Symposium, 1995. Proceedings., 13th IEEE, 30 April-3 May 1995 Pages: 235 - 242 [Abstract] [PDF Full-Text (652 KB)] **IEEE CNF** 

## 4 Accurate logic-level power estimation

Bogliolo, A.; Ricco, B.; Benini, L.; De Micheli, G.; Low Power Electronics, 1995., IEEE Symposium on , 9-11 Oct. 1995 Pages:40 - 41

[Abstract] [PDF Full-Text (212 KB)] IEEE CNF



### 5 Efficient state classification of finite-state Markov chains

Aiguo Xie; Beerel, P.A.;

Computer-Aided Design of Integrated Circuits and Systems, IEEE Transaction

on , Volume: 17 , Issue: 12 , Dec. 1998

Pages:1334 - 1339

[Abstract] [PDF Full-Text (192 KB)] IEEE JNL

# 6 Counterexample-guided choice of projections in approximate symbol model checking

Govindaraju, S.G.; Dill, D.L.;

Computer Aided Design, 2000. ICCAD-2000. IEEE/ACM International Confere

on , 5-9 Nov. 2000 Pages:115 - 119

[Abstract] [PDF Full-Text (448 KB)] IEEE CNF

# 7 Improved reachability analysis of large finite state machines

Cabodi, G.; Camurati, P.; Quer, S.;

Computer-Aided Design, 1996. ICCAD-96. Digest of Technical Papers., 1996

IEEE/ACM International Conference on , 10-14 Nov. 1996

Pages:354 - 360

[Abstract] [PDF Full-Text (620 KB)] IEEE CNF

## 8 SAT-based unbounded symbolic model checking

Hyeong-Ju Kang; In-Cheol Park;

Design Automation Conference, 2003. Proceedings, 2-6 June 2003

Pages:840 - 843

[Abstract] [PDF Full-Text (503 KB)] IEEE CNF

#### 9 Improved SAT-based bounded reachability analysis

Ganai, M.K.; Aziz, A.;

Design Automation Conference, 2002. Proceedings of ASP-DAC 2002. 7th Asia South Pacific and the 15th International Conference on VLSI Design. Proceedings of ASP-DAC 2002. 7th Asia South Pacific and the 15th International Conference on VLSI Design.

, 7-11 Jan. 2002

Pages:729 - 734

[Abstract] [PDF Full-Text (448 KB)] IEEE CNF

## 10 Efficient state classification of finite state Markov chains

Xie, A.; Beerel, P.A.;

Design Automation Conference, 1998. Proceedings, 15-19 June 1998

Pages:605 - 610

[Abstract] [PDF Full-Text (620 KB)] IEEE CNF

Home | Log-out | Journals | Conference Proceedings | Standards | Search by Author | Basic Search | Advanced Search | Join IEEE | Web Account |
New this week | OPAC Linking Information | Your Feedback | Technical Support | Email Alerting | No Robots Please | Release Notes | IEEE Online
Publications | Help | FAQ | Terms | Back to Top

Copyright © 2004 IEEE — All rights reserved